SEAN GREENWOOD

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PROFESSIONAL SUMMARY

Electrical engineering student with experience in hardware validation, PCB design, high-voltage electronics, and system-level debugging. Develops automated test software in C#/.NET and supports semiconductor, medical device, and aerospace electronics through circuit troubleshooting, firmware development, and root cause analysis.

EDUCATION

University of Massachusetts Lowell **2025 - Expected 2027**

Bachelor's degree in Electrical and Electronics Engineering

Middlesex Community College **2022 - 2025**

Associate degree in Electrical and Computer Engineering | GPA: 3.7

TECHNICAL SKILLS

Hardware: PCB design, KiCad, Cadence Allegro, schematic interpretation, hardware validation, high-voltage waveform design, analog-to-digital converters (ADCs), power regulation, RF testing

Software and Test: C# 13, .NET 9, VBA, embedded firmware, automated test development, Teradyne IG-XL, SCPI, GPIB, root cause analysis (RCA)

Design and Fabrication: Autodesk Inventor, 3D printing, design for manufacturability (DFM), aerospace electronics, PCB fabrication and release documentation

PROFESSIONAL EXPERIENCE

Teradyne | Hardware Engineering Intern **Sep 2025 - Present**

- Develop automated self-test software for AI-chip test equipment by converting legacy VBA tools to C# 13/.NET 9 and integrating them into Teradyne IG-XL maintenance workflows.
- Expand device under test (DUT) coverage across board types and channels, including force, sense, smart-power, and leakage testing.
- Design and validate custom loopback and channel-debug PCBs using KiCad and Cadence Allegro.
- Create modular 3D-printed fixtures in Autodesk Inventor to align PCBs, interposers, and DUT assemblies, reducing print time and alignment errors during testing.
- Prepare PCB release and fabrication packages, including part numbers, two-layer stackups, ODDs, attribute forms, and artwork files.

UML LoCST Lab | Undergraduate Research Assistant **May 2026 - Aug 2026**

- Designed and tested aerospace-grade PCBs and components.
- Developed a high-voltage sawtooth waveform (150 V peak-to-peak) to actuate piezoelectric leaflets.

Medtronic | Investigation Test Technician III **Aug 2024 - Aug 2025**

- Interpreted schematics and performed system-level debugging of ADCs, power regulation and delivery circuits, capacitor banks, and timing networks.
- Conducted root cause analysis to identify electrical and thermal causes of failure.
- Supported testing and analysis of high-frequency electronics.

LANGUAGES

English: Fluent | German: Intermediate